

Electronic Acknowledgement Receipt

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Application Number:	10501651
International Application Number:	
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Title of Invention:	On-chip esd protection circuit for compound semiconductor heterojunction bipolar transistor rf circuits
First Named Inventor/Applicant Name:	Yin Tat Ma
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Filer:	Daniel Lewis Dawes
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Attorney Docket Number:	UC1.PAU.05US
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
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1	Extension of Time	SecondRequestForExtensionOfTime.pdf	91463	no	1
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8209	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			99672		
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